

Using Standard Typing Algorithms Incrementally [★]

Matteo Busi¹[0000–0002–5557–8139], Pierpaolo Degano¹[0000–0002–8070–4838], and
Letterio Galletta²[0000–0003–0351–9169]

¹ Dipartimento di Informatica, Università di Pisa, Pisa, Italy
{matteo.busi, degano}@di.unipi.it

² IMT School for Advanced Studies, Lucca, Italy
letterio.galletta@imtlucca.it

Abstract. Modern languages are equipped with static type checking/inference that helps programmers to keep a clean programming style and to reduce errors. However, the ever-growing size of programs and their continuous evolution require building fast and efficient analysers. A promising solution is *incrementality*, aiming at only re-typing the *diffs*, i.e. those parts of the program that change or are inserted, rather than the entire codebase. We propose an algorithmic schema that drives an incremental usage of existing, standard typing algorithms with no changes. Ours is a *grey-box* approach: just the shape of the input, that of the results and some domain-specific knowledge are needed to instantiate our schema. Here, we present the foundations of our approach and the conditions for its correctness. We show it at work to derive two different incremental typing algorithms. The first type checks an imperative language to detect information flow and non-interference, and the second infers types for a functional language. We assessed our proposal on a prototypical implementation of an incremental type checker. Our experiments show that using the type checker incrementally is (almost) always rewarding.

1 Introduction

Most of the modern programming languages are equipped with mechanisms for checking or inferring types. Such static analyses prescribe programmers a clean programming style and help them to reduce errors. The ever-growing size of programs requires building fast and efficient analyzers. This quest becomes even more demanding because many companies are recently adopting development methodologies that advocate a continuous evolution of software, e.g. *perpetual development model* [4]. In such a model a shared code base is altered by many programmers submitting small code modifications (*diffs*). Software systems are no longer monolithic pieces of code, to which only new components/modules can be compositionally added, rather their components grow and change incrementally.

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Consequently, analyses and verification should only consider the *diffs*, rather than the entire codebase. As recently observed by [7], it becomes crucial defining algorithms that require an amount of work on the size of the *diffs* instead of the whole codebase. The idea is to store summaries of the analysis results for program components, and to only reanalyze changed parts, re-using the cached summaries. Here we formalise this idea focussing on type systems.

The literature reports on some techniques, briefly surveyed below, which introduce *new* typing algorithms that work incrementally. Instead, we propose a method that takes an *existing* typing algorithm and *uses it incrementally*, without re-doing work already done, but exploiting available summaries, through caching and memoization. An advantage of our proposal is that it consists of an algorithmic schema *independent* of any specific language and type system. In addition, we put forward a mild condition on the summaries that guarantees that the results of incremental typing match those of the original algorithm.

Roughly, our schema works as follows. We start from the abstract syntax tree of the program, where each node is annotated with the result R provided by the original typing algorithm \mathcal{A} . We build then a cache, containing for each subterm t the result R and other relevant contextual information needed by \mathcal{A} to type t (typically a typing environment binding the free variables of t). When the program changes, its annotated abstract syntax tree changes accordingly and typing the subterm associated with the changed node is done incrementally, by reusing the results in the cache whenever possible and by suitably invoking \mathcal{A} upon need. Clearly, the more local the changes, the more information is reused.

Technically, our proposal consists of a set of rule schemata that drive the usage of the cache and of the original algorithm \mathcal{A} , as sketched above. Actually, the user has to define the shape of caches and to instantiate a well-confined part of the rule schemata. If the instantiation meets an easy-to-check criterion, the typing results of \mathcal{A} and of the incremental algorithm are guaranteed to be coherent, i.e. the incremental algorithm behaves as the non-incremental one. All the above provides us with the guidelines to develop a framework that makes incremental the usage of a given typing algorithm.

Summing up, the main contributions of this paper include:

- a parametric, language-independent algorithmic schema that uses an existing typing algorithm \mathcal{A} incrementally (Section 3);
- a formalisation of the steps that instantiate the schema and yield the incremental version of \mathcal{A} : the resulting typing algorithm only types the *diffs* and those parts of the code affected by them (Section 3);
- a characterisation of the rule format of standard typing algorithms in terms of two functions tr and $checkJoin$ (Section 3);
- a theorem that under a mild condition guarantees the coherence of results between the original algorithm and its incremental version (Section 3);
- the instantiation of the schema for type checking and type inference algorithm for an imperative (Section 4) and a functional language (Section 5);

- a prototype of the incremental version of the type checker for MinCaml [20],³ showing that implementing the schema is doable (Section 6); and
- experimental results showing that the cost of using the type checker incrementally depends on the size of *diffs*, and its performance increases as these become smaller (Section 6).

All the proofs of our theorems, some additional material and the tables with the experimental results on the time and space overheads are in the extended version available online [3].

Related work. To the best of our knowledge, the literature has some proposals for incrementally typing programs. However, these approaches heavily differ from ours, because all of them propose a *new* incremental algorithm for typing, while we *incrementally* use *existing* algorithms as they are. Additionally, none of the approaches surveyed below use a uniform characterisation of type judgements as we do through the metafunctions *tr* and *checkJoin*.

Meertens [13] proposes an incremental type checking algorithm for the language B. Johnson and Walz [9] treat incremental type inference, focussing on identifying where type errors precisely arise. Aditya and Nikhil [1] propose an incremental Hindley/Milner type system supporting incremental type checking of top-level definitions. Our approach instead supports incremental type-checking for all kinds of expressions, not only the top-level ones. Miao and Siek [14] introduce an incremental type checker leveraging the fact that, in multi-staged programming, programs are successively refined. Wachsmuth et al. [23] propose a task engine for type checking and name resolution: when a file is modified a task is generated and existing (cached) results are re-used where possible. The proposal by Erdweg et al. [5] is the most similar to ours, but, given a type checking algorithm, they describe how to obtain a *new* incremental algorithm. As in our case, they decorate an abstract syntax tree with types and typing environments, represented as sets of constraints, to be suitably propagated when typing. In this way there is no need of dealing with top-down context propagation while types flow bottom-up. Recently, Facebook released Pyre [6] a scalable and incremental type checker specifically designed for Python.

Incrementality has also been studied for static analysis other than typing. IncA [21] is a domain-specific language for the definition of incremental program analyses, which represents dependencies among the nodes of the abstract syntax tree of the target program as a graph. Infer [8] uses an approach similar to ours in which analysis results are cached to improve performance [2]. Ryder and Paull [18] present two incremental update algorithms, ACINCB and ACINCF, that allow incremental data-flow analysis. Yur et al. [26] propose an algorithm for an incremental *points-to* analysis. McPeak et al. [12] describe a technique for incremental and parallel static analysis based on *work units* (self-contained atoms of analysis input). The solutions are computed by a sort of processes called *analysis workers*, all coordinated by an *analysis master*. Also, there are papers that use memoization with a goal similar to the one of our cache, even if they

³ Available at <https://github.com/mcaos/incremental-mincaml>

consider different analysis techniques. In particular, Mudduluru et al. propose, implement, and test an incremental analysis algorithm based on memoization of (equivalent) boolean formulas used to encode paths on programs [15]. Leino et al. [11] extend the verification machinery of Dafny with a way to cache the results from earlier runs of the verifier, so as to only verify those parts of the program most recently modified. Their cache mechanism is similar to ours, but no formal condition is explicitly stated guaranteeing a safe re-use of cached data, as we do. Also other authors apply memoization techniques to incremental model-checking [10,24] and incremental symbolic execution [25,17].

2 An overview of the incremental schema

In this section we illustrate how the algorithmic schema we propose can type check a simple program using incrementally the *standard* algorithm for non-interference by Volpano-Smith-Irvine [22,19] (see also Section 4). Suppose there is a large program P including the following code snippet that causes no leaks

$$x := y + z; \text{if } y + z \geq 42 \text{ then } result := y + z \text{ else } result := 42 \quad (1)$$

where $y, z, result$ are public and x is secret. After a shallow glance one may optimise it obtaining the following code that leaks the value of x

$$x := y + z; \text{if } x \geq 42 \text{ then } result := x \text{ else } result := 42 \quad (2)$$

Rather than re-typing the whole program P , one would like to detect the unsafe optimization by only type checking the *diff*, i.e. the *if-then-else*. To re-use as much as possible the typing information of P , we consider the abstract syntax tree of (1), we annotate its nodes with types, and we use this information to type (2). More precisely, we proceed as follows.

First, we build a *cache* C associating each statement with its type and the typing environment needed to obtain it. Then we *incrementally* use this information to decide which existing results in the cache can be re-used and which are to be recomputed for typing (2). This process is divided into four steps. For the moment, we omit the last one that consists in proving the correctness of the resulting algorithm, which is established by showing that a component of our construction (the predicate $compat_{env}$ used below) meets a mild condition.

Defining the shape of caches. The cache is a set of triples that associate with each statement the typing environment needed to close its free variables, and its type. For example, the first statement has the following entry in the cache, recording in the environment the types of the variables (H and L for secret and public) and the type $H\ cmd$ of the assignment (it is a command of type H),

$$(x := y + z, \{x \mapsto H\ var, y \mapsto L\ var, z \mapsto L\ var\}, H\ cmd) \quad (3)$$

Building caches. We visit the given annotated abstract syntax tree of (1) in a depth-first order and we cache the relevant triples for it and for its (sub-)trees. Consider again the first assignment for which the cache records the triple in (3), among others. All the entries for (1) are in Table 1.

Table 1: Tabular representation of the cache C for the program (1).

Expression	Environment	Type
(1)	$\{x \mapsto H \text{ var}, y \mapsto L \text{ var}, z \mapsto L \text{ var}, \text{result} \mapsto L \text{ var}\}$	$L \text{ cmd}$
$x := y + z$	$\{x \mapsto H \text{ var}, y \mapsto L \text{ var}, z \mapsto L \text{ var}\}$	$H \text{ cmd}$
x	$\{x \mapsto H \text{ var}\}$	H
y	$\{y \mapsto L \text{ var}\}$	L
z	$\{z \mapsto L \text{ var}\}$	L
result	$\{\text{result} \mapsto L \text{ var}\}$	L
42	$\{\}$	L
$y + z$	$\{y \mapsto L \text{ var}, z \mapsto L \text{ var}\}$	L
if $y + z \geq 42$ then $\text{result} := y + z$ else $\text{result} := 42$	$\{y \mapsto L \text{ var}, z \mapsto L \text{ var}, \text{result} \mapsto L \text{ var}\}$	$L \text{ cmd}$
$y + z \geq 42$	$\{y \mapsto L \text{ var}, z \mapsto L \text{ var}\}$	L
$\text{result} := y + z$	$\{y \mapsto L \text{ var}, z \mapsto L \text{ var}, \text{result} \mapsto L \text{ var}\}$	$L \text{ cmd}$
$\text{result} := 42$	$\{\text{result} \mapsto L \text{ var}\}$	$L \text{ cmd}$

Incremental typing. The selected typing algorithm \mathcal{S} is used to build the incremental algorithm \mathcal{IS} as follows. A judgement inputs an environment Γ , a cache C and a statement c and it incrementally computes the type ς (see Section 4) and C' , with possibly updated cache entries for the sub-terms of c :

$$\Gamma, C \vdash_{\mathcal{IS}} c : \varsigma \triangleright C'$$

The incremental algorithm is expressed as a set of inductively defined rules. Most of these simply mimic the structure of the rules defining \mathcal{S} . Consider the assignment that requires two rules. The first rule says that we can reuse the information available if the statement is cached and the environments Γ and Γ' coincide on the free variables of c (checked by the predicate $\text{compat}_{\text{env}}(\Gamma, \Gamma', c)$):

$$\frac{C(x := a) = \langle \Gamma', \varsigma \rangle \quad \text{compat}_{\text{env}}(\Gamma, \Gamma', x := a)}{\Gamma, C \vdash_{\mathcal{IS}} x := a : \varsigma \triangleright C}$$

The second rule is for when nothing is cached (the side condition *miss* holds), or the typing environments are not compatible. In this case, to obtain C' , the new cache, is obtained from C by inserting in it the triples for x , for the expression a and for the assignment itself through \mathcal{IS} .

$$\frac{\Gamma, C \vdash_{\mathcal{IS}} x : \tau_x \triangleright C'' \quad \Gamma, C \vdash_{\mathcal{IS}} a : \tau_a \triangleright C''' \quad \tau_a = \tau_x \quad \varsigma = \tau_a \text{ cmd} \quad C' = C'' \cup C''' \cup \{(x := a, \Gamma|_{FV(x:=a)}, \varsigma)\}}{\Gamma, C \vdash_{\mathcal{IS}} x := a : \varsigma \triangleright C'} \quad \text{miss}(C, x := a, \Gamma)$$

Back to our example, to discover the leak it suffices to type $x \geq 42$ and $\text{result} := x$ having already in the cache the types of x and result , while no re-typing is needed for all the other statements of the whole program P .

3 Formalizing the incremental schema

Here we formalise our algorithmic schema for incremental typing, exemplified in Section 2. Remarkably, it is independent of both the specific type system

and the programming language (for that we use below $t \in Term$ to denote an expression or a statement). We only assume to have variables $x, y, \dots \in Var$, types $\tau, \tau', \dots \in Type$, typing environments $\Gamma: Var \rightarrow Type \in Env$; and in addition that the original typing algorithm \mathcal{A} is syntax-directed and defined through inference rules; that it is invoked by writing $\Gamma \vdash_{\mathcal{A}} t: R$, where $R \in Res$ is the result (not necessarily a type only).

Below we express the rules of \mathcal{A} according to the following format. It is convenient to order the subterms of t , by stipulating $i \leq j$ provided that t_j requires the result of t_i to be typed ($i, j \leq n_t$).

$$\frac{\forall i \in \mathbb{I}_t. tr_{t_i}^t(\Gamma, \{R_j\}_{j < i \wedge j \in \mathbb{I}_t}) \vdash_{\mathcal{A}} t_i : R_i \quad checkJoin_t(\Gamma, \{R_i\}_{i \in \mathbb{I}_t}, \mathbf{out} R)}{\Gamma \vdash_{\mathcal{A}} t : R}$$

where $\mathbb{I}_t \subseteq \{1, \dots, n_t\}$. The function $tr_{t_i}^t$ maps Γ and a set of typing results R_i into the typing environment needed by t_i . The (conjunction of) predicate(s) $checkJoin_t$ checks that the subterms have compatible results R_i and combines them in the overall result R . (Both tr and $checkJoin$ are easily defined when typing rules in the usual format are rendered in the format above.)

For example the standard typing rule for variables:⁴

$$\frac{x \in dom(\Gamma) \quad \tau = \Gamma(x)}{\Gamma \vdash_{\mathcal{A}} x : \tau}$$

is rendered in our format as follows (note that $\mathbb{I}_x = \emptyset$ just as the function tr)

$$\frac{checkJoin_x(\Gamma, \emptyset, \mathbf{out} \tau)}{\Gamma \vdash_{\mathcal{A}} x : \tau} \quad \text{where } checkJoin_x(\Gamma, \emptyset, \mathbf{out} \tau) \triangleq x \in dom(\Gamma) \wedge \tau = \Gamma(x)$$

As a further example consider the rule for the expression $\mathbf{let} x = e_2 \mathbf{in} e_3$ below

$$\frac{\Gamma \vdash_{\mathcal{A}} e_2 : \tau_2 \quad \Gamma[x \mapsto \tau_2] \vdash_{\mathcal{A}} e_3 : \tau_3}{\Gamma \vdash_{\mathcal{A}} \mathbf{let} x = e_2 \mathbf{in} e_3 : \tau_3}$$

that becomes as follows (we abuse the set notation, e.g. omitting \emptyset or $\{$ and $\}$).

$$\frac{tr_{e_2}^{\mathbf{let} x = e_2 \mathbf{in} e_3}(\Gamma, \emptyset) \vdash_{\mathcal{A}} e_2 : \tau_2 \quad tr_{e_3}^{\mathbf{let} x = e_2 \mathbf{in} e_3}(\Gamma, \tau_2) \vdash_{\mathcal{A}} e_3 : \tau_3 \quad checkJoin_{\mathbf{let} x = e_2 \mathbf{in} e_3}(\Gamma, \tau_2, \tau_3, \mathbf{out} \tau)}{\Gamma \vdash_{\mathcal{A}} \mathbf{let} x = e_2 \mathbf{in} e_3 : \tau}$$

Note that the definition of function tr is immediate; that we need the type of e_2 for typing e_3 ; and that the second parameter of $tr_{e_2}^{\mathbf{let} x = e_2 \mathbf{in} e_3}$ is empty, because we only need the environment to type e_2 .

$$tr_{e_2}^{\mathbf{let} x = e_2 \mathbf{in} e_3}(\Gamma, \emptyset) \triangleq \Gamma \quad tr_{e_3}^{\mathbf{let} x = e_2 \mathbf{in} e_3}(\Gamma, \tau) \triangleq \Gamma[x \mapsto \tau] \quad (4)$$

⁴ Instead with the axiom $\Gamma[x \mapsto \tau] \vdash_{\mathcal{A}} x : \tau$ one has $\mathbb{I}_x = \emptyset$ and the same $checkJoin_x$, where $\Gamma = \Gamma[x \mapsto \tau]$.

Also the following definition is immediate

$$\mathit{checkJoin}_{\mathbf{let} \ x = e_2 \ \mathbf{in} \ e_3}(\Gamma, \tau_2, \tau_3, \mathbf{out} \ \tau) \triangleq (\tau = \tau_3)$$

To enhance readability, we will hereto highlight the occurrences of $\boxed{tr_{t'}^t}$ (red in the pdf) and $\boxed{\mathit{checkJoin}_t}$ (blue in the pdf).

Defining the shape of caches. The shape of the cache is crucial for re-using incrementally portions of the available typing results. A cache associates the input data t and Γ with the result R , rendered by a set of triples (t, Γ, R) , as done in Section 2. More formally, the set of caches C is defined as:

$$\mathit{Cache} = \wp(\mathit{Terms} \times \mathit{Env} \times \mathit{Res})$$

We write $C(t) = \langle \Gamma, R \rangle$ if the cache has an entry for t , and $C(t) = \perp$ otherwise.

Building caches. Given a term, we assume that the nodes of its abstract syntax tree (called *annotated abstract syntax tree* or *aAST*) are annotated with the result of the typing for the subterm they represent (written $t : R$, possibly $t : \perp$ if t does not type). Let \mathbb{I}_t , $\{t_i\}_{i \in \mathbb{I}_t}$, and $tr_{t_i}^t$ be as above, and let $\Gamma|_{FV(t)}$ be the restriction of Γ to the free variables of t . Then the following procedure visits the aAST in a depth-first manner and builds the cache.

$$\begin{aligned} \mathit{buildCache} \ (t : R) \ \Gamma = & \{(t, \Gamma|_{FV(t)}, R)\} \cup \\ & \bigcup_{i \in \mathbb{I}_t} (\mathit{buildCache} \ (t_i : R_i) \ \boxed{tr_{t_i}^t(\Gamma, \{R_j\}_{j < i \wedge j \in \mathbb{I}_t})}) \end{aligned}$$

The following theorem ensures that each entry of a cache returned by $\mathit{buildCache}$ represents correct typing information.

Theorem 1 (Cache correctness). *For all t, R, Γ*

$$(t, \Gamma, R) \in (\mathit{buildCache} \ (t : R) \ \Gamma) \iff \Gamma \vdash_{\mathcal{A}} t : R$$

Incremental typing. The third step consists of instantiating the rule templates that make typing incremental. We remark that no change to the original algorithm \mathcal{A} is needed: it is used as a *grey-box* — what matters are just the shape of the original judgements, the rules and some domain-specific knowledge. The judgements for the incremental typing algorithm \mathcal{IA} have the form:

$$\Gamma, C \vdash_{\mathcal{IA}} t : R \triangleright C'$$

We have three different rule templates defining the incremental typing algorithm. The first template is for the case when there is a cache hit:

$$\frac{C(t) = \langle \Gamma', R \rangle \quad \mathit{compat}_{env}(\Gamma, \Gamma', t)}{\Gamma, C \vdash_{\mathcal{IA}} t : R \triangleright C}$$

where $compat_{env}(\Gamma, \Gamma', t)$ is a predicate testing the compatibility of typing environments for the term t and means that Γ' includes the information represented by Γ for t and that they are compatible (see the example in Section 2). Note that this predicate must be defined for *each* algorithm \mathcal{A} and, as discussed below, it must meet a mild requirement to make the algorithm \mathcal{IA} coherent with \mathcal{A} .

The second rule template is for when there is a cache miss and the term in hand has no subterms:

$$\frac{\Gamma \vdash_{\mathcal{A}} t : R \quad C' = C \cup \{(t, \Gamma|_{FV(t)}, R)\}}{\Gamma, C \vdash_{\mathcal{IA}} t : R \triangleright C'} \text{miss}(C, t, \Gamma)$$

where $\Gamma \vdash_{\mathcal{A}} t : R$ is the invocation to \mathcal{A} , and the predicate $miss$ is defined below with the intuition that either there is no association for t in C , or if an association (t, Γ', R) exists the typing environment Γ' is not compatible with the current Γ .

$$miss(C, t, \Gamma) \triangleq \nexists \Gamma', R. (C(t) = \langle \Gamma', R \rangle \wedge compat_{env}(\Gamma, \Gamma', t))$$

Finally, the last template applies when there is a cache miss, but the term t is inductively defined starting from its subterms. In this case the rule invokes the incremental algorithm on the subterms, by composing the results available in the cache (if any):

$$\frac{\forall i \in \mathbb{I}_t. \boxed{tr_t^{t_i}(\Gamma, \{R_j\}_{j < i \wedge j \in \mathbb{I}_t})}, C \vdash_{\mathcal{IA}} t_i : R_i \triangleright C^i \quad \boxed{checkJoin_t(\Gamma, \{R_i\}_{i \in \mathbb{I}_t}, out R)} \quad C' = \{(t, \Gamma|_{FV(t)}, R)\} \cup \bigcup_{i \in \mathbb{I}_t} C^i}{\Gamma, C \vdash_{\mathcal{IA}} t : R \triangleright C'} \text{miss}(C, t, \Gamma)$$

Typing coherence. The resulting algorithm \mathcal{IA} preserves the correctness of the original one \mathcal{A} , provided that the rule templates above, and especially the predicate $compat_{env}$ are carefully instantiated.

The following definition characterises when two environments are compatible, and it helps in proving that our incremental typing correctly implements the given non-incremental one.

Definition 1 (Typing environment compatibility). A predicate $compat_{env}$ expresses compatibility iff

$$\forall \Gamma, \Gamma', t. compat_{env}(\Gamma, \Gamma', t) \wedge \Gamma' \vdash_{\mathcal{A}} t : R \implies \Gamma \vdash_{\mathcal{A}} t : R$$

Note that the notion of compatibility guarantees that Γ and Γ' share all the information needed to correctly type the term t . This is the basic condition to ensure that the incremental typing algorithm is concordant with the original one. In particular, the following theorem suffices to establish the correctness of the incremental algorithm \mathcal{IA} , provided that the original algorithm \mathcal{A} is such. In its statement, the cache is universally quantified because \mathcal{IA} re-uses \mathcal{A} to re-build the needed cache as soon as a cache miss occurs.

Theorem 2 (Typing coherence). If $compat_{env}$ expresses compatibility, then for all terms t , caches C , typing environments Γ , and typing algorithm \mathcal{A}

$$\Gamma \vdash_{\mathcal{A}} t : R \iff \Gamma, C \vdash_{\mathcal{IA}} t : R \triangleright C'.$$

4 Incremental type checking for non-interference

Here we use incrementally the typing algorithm \mathcal{S} of Volpano-Smith-Irvine [22,19] for checking non-interference policies, obtaining the algorithm \mathcal{IS} . We assume that the variables of programs are classified either as high, H , or low L . Intuitively, a program enjoys the non-interference property when the values of low level variables do not depend on those of high level ones.

As usual, assume a simple imperative language `WHILE`, whose syntax is below (Var denotes the set of program variables).

$$\begin{aligned}
 AExpr \ni a &::= n \mid x \mid a_1 \text{ op}_a a_2 & n \in \mathbb{N}, \quad \text{op}_a \in \{+, *, -, \dots\}, \quad x \in Var \\
 BExpr \ni b &::= \text{true} \mid \text{false} \mid b_1 \text{ or } b_2 \mid \text{not } b \mid a_1 \leq a_2 \\
 Stmt \ni c &::= \text{skip} \mid x := a \mid c_1; c_2 \mid \text{if } b \text{ then } c_1 \text{ else } c_2 \mid \text{while } b \text{ do } c \\
 Phrase \ni p &::= a \mid b \mid c \\
 DType \ni \tau &::= H \mid L \quad PType \ni \varsigma ::= \tau \mid \tau \text{ var} \mid \tau \text{ cmd} \quad Env \ni \Gamma ::= \emptyset \mid \Gamma[p \mapsto \varsigma]
 \end{aligned}$$

The type checking algorithm has judgements of the form

$$\Gamma \vdash_{\mathcal{S}} p : \varsigma$$

where $\varsigma \in PType = Res$, and its rules are in the extended version [3]. We have coloured and framed the results of `tr` and `checkJoin`. In the following we assume that the initial typing environment Γ contains the security level of each variable occurring in the program at hand.

Defining the shape of caches. The shape of the caches is:

$$C \in Cache = \wp(Phrase \times Env \times PType)$$

Building caches. We build the cache by visiting the aAST and “reconstructing” the typing environment. The function `buildCache` is in Fig. 1, where for brevity we have directly used the results of `tr` rather than writing the needed invocations.

Incremental typing. In Fig. 2 we display the rules defining the algorithm \mathcal{IS} with judgements of the following form

$$\Gamma, C \vdash_{\mathcal{IS}} p : \varsigma \triangleright C'$$

Most of the rules are trivial instantiations of rules in Section 3 that mimic those of the original type checking algorithm. Of course, \mathcal{IS} inherits unchanged the subtyping relation of \mathcal{S} and applies it when needed.

Typing coherence. To prove that \mathcal{IS} is coherent with \mathcal{S} , we first show that $compat_{env}$ satisfies Definition 1.

Lemma 1. *The predicate $compat_{env}$ of Eq. (5) in Fig. 2 expresses compatibility.*

The above lemma suffices to prove the following theorem, which is an instance of Theorem 2.

Theorem 3. $\forall \Gamma, C, e. \Gamma \vdash_{\mathcal{S}} e : \tau \iff \Gamma, C \vdash_{\mathcal{IS}} e : \tau \triangleright C'$

$$\begin{aligned}
\mathit{buildCache} (c : L) \Gamma &\triangleq \{(c, \emptyset, L)\} \quad c \in \mathbb{N} \cup \{\mathit{true}, \mathit{false}\} \\
\mathit{buildCache} (x : \tau) \Gamma &\triangleq \{(x, [x \mapsto \tau \mathit{var}], \tau)\} \\
\mathit{buildCache} (a_1 \mathit{op} a_2 : \tau) \Gamma &\triangleq \{(a_1 \mathit{op} a_2, \Gamma|_{FV(a_1 \mathit{op} a_2)}, \tau)\} \\
&\cup (\mathit{buildCache} (a_1 : \tau_1) \boxed{\Gamma}) \cup (\mathit{buildCache} (a_2 : \tau_2) \boxed{\Gamma}) \\
\mathit{buildCache} (a_1 \leq a_2 : \tau) \Gamma &\triangleq \{(a_1 \leq a_2, \Gamma|_{FV(a_1 \leq a_2)}, \tau)\} \\
&\cup (\mathit{buildCache} (a_1 : \tau_1) \boxed{\Gamma}) \cup (\mathit{buildCache} (a_2 : \tau_2) \boxed{\Gamma}) \\
\mathit{buildCache} (b_1 \mathit{or} b_2 : \tau) \Gamma &\triangleq \{(b_1 \mathit{or} b_2, \Gamma|_{FV(b_1 \mathit{or} b_2)}, \tau)\} \\
&\cup (\mathit{buildCache} (b_1 : \tau_1) \boxed{\Gamma}) \cup (\mathit{buildCache} (b_2 : \tau_2) \boxed{\Gamma}) \\
\mathit{buildCache} (\mathit{not} b : \tau) \Gamma &\triangleq \{(\mathit{not} b, \Gamma|_{FV(\mathit{not} b)}, \tau)\} \cup (\mathit{buildCache} (b : \tau) \boxed{\Gamma}) \\
\mathit{buildCache} (\mathit{skip} : H \mathit{cmd}) \Gamma &\triangleq \{(\mathit{skip}, \emptyset, H \mathit{cmd})\} \\
\mathit{buildCache} (x := a : \tau \mathit{cmd}) \Gamma &\triangleq \{(x := a, \Gamma|_{FV(x:=a)}, \tau \mathit{cmd})\} \\
&\cup (\mathit{buildCache} (x : \tau_x) \boxed{\Gamma}) \cup (\mathit{buildCache} (a : \tau_a) \boxed{\Gamma}) \\
\mathit{buildCache} (\mathit{if} b \mathit{then} c_1 \mathit{else} c_2 : \tau \mathit{cmd}) \Gamma &\triangleq \{(\mathit{if} b \mathit{then} c_1 \mathit{else} c_2, \Gamma|_{FV(\mathit{if} b \mathit{then} c_1 \mathit{else} c_2)}, \tau \mathit{cmd})\} \\
&\cup (\mathit{buildCache} (b : \tau_b) \boxed{\Gamma}) \cup (\mathit{buildCache} (c_1 : \tau_1 \mathit{cmd}) \boxed{\Gamma}) \cup (\mathit{buildCache} (c_2 : \tau_2 \mathit{cmd}) \boxed{\Gamma}) \\
\mathit{buildCache} (\mathit{while} b \mathit{do} c : \tau \mathit{cmd}) \Gamma &\triangleq \{(\mathit{while} b \mathit{do} c, \Gamma|_{FV(\mathit{while} b \mathit{do} c)}, \tau \mathit{cmd})\} \\
&\cup (\mathit{buildCache} (b : \tau_b) \boxed{\Gamma}) \cup (\mathit{buildCache} (c : \tau_c \mathit{cmd}) \boxed{\Gamma}) \\
\mathit{buildCache} (c_1; c_2 : \tau \mathit{cmd}) \Gamma &\triangleq \{(c_1; c_2, \Gamma|_{FV(c_1; c_2)}, \tau \mathit{cmd})\} \\
&\cup (\mathit{buildCache} (c_1 : \tau_1 \mathit{cmd}) \boxed{\Gamma}) \cup (\mathit{buildCache} (c_2 : \tau_2 \mathit{cmd}) \boxed{\Gamma})
\end{aligned}$$

Fig. 1: Definition of $\mathit{buildCache}$ for the incremental type checking of `WHILE`.

5 Incremental type inference for a functional language

In this section we instantiate our schema in order to use incrementally the type inference algorithm of a simple functional programming language, called `FUN`. The syntax, the types and the semantics of `FUN` are standard, see e.g. [16]. Types are also now augmented with type variables $\alpha, \beta, \dots \in TVar$. We only recall some relevant aspects below.

$$\begin{aligned}
\mathit{Val} \ni v &::= c \mid \lambda_f x.e & \mathit{op} &\in \{+, *, =, \leq\} \\
\mathit{Expr} \ni e &::= v \mid x \mid e_1 \mathit{op} e_2 \mid e_1 e_2 \mid \mathit{if} e_1 \mathit{then} e_2 \mathit{else} e_3 \mid \mathit{let} x = e_2 \mathit{in} e_3 \\
\mathit{AType} \ni \tau &::= \mathit{int} \mid \mathit{bool} \mid \tau_1 \rightarrow \tau_2 \mid \alpha & \mathit{Env} \ni \Gamma &::= \emptyset \mid \Gamma[x \mapsto \tau]
\end{aligned}$$

where in the functional abstraction f denotes the name of the (possibly) recursive function we are defining. The judgements of the type inference algorithm \mathcal{W} are

$$\Gamma \vdash_{\mathcal{W}} e : (\tau, \theta)$$

where $\theta : (TVar \rightarrow AType) \in Subst$ is a substitution mapping type variables into augmented types. As usual, we write $\theta \tau$ to indicate the application of the substitution θ to τ , and $\theta_2 \circ \theta_1$ stands for the composition of substitutions.

Hereafter, we assume to use the inference algorithm \mathcal{W} (see e.g. [16]), where constants c have a fixed and known type, and \mathcal{U} denotes the standard type unification algorithm.

$$\begin{array}{c}
\text{(IS-HIT)} \quad \frac{C(p) = \langle \Gamma', \varsigma \rangle \quad \text{compat}_{\text{env}}(\Gamma, \Gamma', p)}{\Gamma, C \vdash_{\text{IS}} p : \varsigma \triangleright C} \quad \text{(IS-CONST-MISS)} \quad \frac{\emptyset \vdash_{\text{S}} c : \varsigma \quad C' = C \cup \{(c, \emptyset, \varsigma)\}}{\Gamma, C \vdash_{\text{IS}} c : \varsigma \triangleright C'} \text{miss}(C, c, \Gamma) \\
\\
\text{(IS-VAR-MISS)} \quad \frac{\Gamma \vdash_{\text{S}} x : \varsigma \quad C' = C \cup \{(x, \Gamma|_x, \varsigma)\}}{\Gamma, C \vdash_{\text{IS}} x : \varsigma \triangleright C'} \text{miss}(C, x, \Gamma) \quad \text{(IS-SKIP-MISS)} \quad \frac{\text{miss}(C, \text{skip}, \Gamma)}{\Gamma \vdash_{\text{S}} \text{skip} : \varsigma \quad C' = C \cup \{(\text{skip}, \emptyset, \varsigma)\}} \frac{}{\Gamma, C \vdash_{\text{IS}} \text{skip} : \varsigma \triangleright C'} \\
\\
\text{(IS-OP-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\text{IS}} a_1 : \tau_1 \triangleright C'' \quad \boxed{\Gamma}, C \vdash_{\text{IS}} a_2 : \tau_2 \triangleright C''' \quad \boxed{\tau_1 \equiv \tau_2 \wedge \xi \equiv \tau_1}}{C' = C'' \cup C''' \cup \{(a_1 \text{ op } a_2, \Gamma|_{FV(a_1 \text{ op } a_2)}, \varsigma)\}} \frac{}{\Gamma, C \vdash_{\text{IS}} a_1 \text{ op } a_2 : \varsigma \triangleright C'} \text{miss}(C, a_1 \text{ op } a_2, \Gamma) \\
\\
\text{(IS-BOP-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\text{IS}} b_1 : \tau_1 \triangleright C'' \quad \boxed{\Gamma}, C \vdash_{\text{IS}} b_2 : \tau_2 \triangleright C''' \quad \boxed{\tau_1 \equiv \tau_2 \wedge \xi \equiv \tau_1}}{C' = C'' \cup C''' \cup \{(b_1 \text{ or } b_2, \Gamma|_{FV(b_1 \text{ or } b_2)}, \varsigma)\}} \frac{}{\Gamma, C \vdash_{\text{IS}} b_1 \text{ or } b_2 : \varsigma \triangleright C'} \text{miss}(C, b_1 \text{ or } b_2, \Gamma) \\
\\
\text{(IS-NOT-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\text{IS}} b : \tau \triangleright C'' \quad C' = C'' \cup \{(\text{not } b, \Gamma|_{FV(\text{not } b)}, \tau)\}}{\boxed{\Gamma}, C \vdash_{\text{IS}} \text{not } b : \tau \triangleright C'} \text{miss}(C, \text{not } b, \Gamma) \\
\\
\text{(IS-LEQ-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\text{IS}} a_1 : \tau_1 \triangleright C'' \quad \boxed{\Gamma}, C \vdash_{\text{IS}} a_2 : \tau_2 \triangleright C''' \quad \boxed{\tau_1 \equiv \tau_2 \wedge \xi \equiv \tau_1}}{C' = C'' \cup C''' \cup \{(a_1 \leq a_2, \Gamma|_{FV(a_1 \leq a_2)}, \varsigma)\}} \frac{}{\Gamma, C \vdash_{\text{IS}} a_1 \leq a_2 : \varsigma \triangleright C'} \text{miss}(C, a_1 \leq a_2, \Gamma) \\
\\
\text{(IS-ASSIGN-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\text{IS}} x : \tau_x \text{ var} \triangleright C'' \quad \boxed{\Gamma}, C \vdash_{\text{IS}} a : \tau_a \triangleright C''' \quad \boxed{\tau_x \equiv \tau_a \wedge \xi \equiv \tau_a \text{ cmd}}}{C' = C'' \cup C''' \cup \{(x := a, \Gamma|_{FV(x:=a)}, \varsigma)\}} \frac{}{\Gamma, C \vdash_{\text{IS}} x := a : \varsigma \triangleright C'} \text{miss}(C, x := a, \Gamma) \\
\\
\text{(IS-IF-MISS)} \quad \frac{\text{miss}(C, \text{if } b \text{ then } c_1 \text{ else } c_2, \Gamma) \quad \boxed{\Gamma}, C \vdash_{\text{IS}} c_1 : \tau_1 \text{ cmd} \triangleright C''' \quad \boxed{\Gamma}, C \vdash_{\text{IS}} b : \tau_b \triangleright C'' \quad \boxed{\Gamma}, C \vdash_{\text{IS}} c_2 : \tau_2 \text{ cmd} \triangleright C^{iv} \quad \boxed{\tau_1 \equiv \tau_2 \equiv \tau_b \wedge \xi \equiv \tau_1 \text{ cmd}}}{C' = C'' \cup C''' \cup C^{iv} \cup \{(\text{if } b \text{ then } c_1 \text{ else } c_2, \Gamma|_{FV(\text{if } b \text{ then } c_1 \text{ else } c_2)}, \varsigma)\}} \frac{}{\Gamma, C \vdash_{\text{IS}} \text{if } b \text{ then } c_1 \text{ else } c_2 : \varsigma \triangleright C'} \\
\\
\text{(IS-WHILE-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\text{IS}} c : \tau_1 \text{ cmd} \triangleright C''' \quad \boxed{\Gamma}, C \vdash_{\text{IS}} b : \tau_b \triangleright C'' \quad \boxed{\tau_1 \equiv \tau_b \wedge \xi \equiv \tau_1 \text{ cmd}}}{C' = C'' \cup C''' \cup \{(\text{while } b \text{ do } c, \Gamma|_{FV(\text{while } b \text{ do } c)}, \varsigma)\}} \frac{}{\Gamma, C \vdash_{\text{IS}} \text{while } b \text{ do } S : \varsigma \triangleright C'} \text{miss}(C, \text{while } b \text{ do } c, \Gamma) \\
\\
\text{(IS-SEQ-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\text{IS}} c_1 : \tau_1 \text{ cmd} \triangleright C'' \quad \boxed{\Gamma}, C \vdash_{\text{IS}} c_2 : \tau_2 \text{ cmd} \triangleright C''' \quad \boxed{\tau_1 \equiv \tau_2 \wedge \xi \equiv \tau_1 \text{ cmd}}}{C' = C'' \cup C''' \cup \{(c_1; c_2, \Gamma|_{FV(c_1; c_2)}, \varsigma)\}} \frac{}{\Gamma, C \vdash_{\text{IS}} c_1; c_2 : \varsigma \triangleright C'} \text{miss}(C, c_1; c_2, \Gamma)
\end{array}$$

with $\text{compat}_{\text{env}}(\Gamma, \Gamma', p) \triangleq \text{dom}(\Gamma) \supseteq \text{FV}(p) \wedge \text{dom}(\Gamma') \supseteq \text{FV}(p) \wedge \forall y \in \text{FV}(p). \Gamma(y) = \Gamma'(y)$ (5)

Fig. 2: Rules defining the incremental algorithm IS to type check WHILE.

$$\begin{aligned}
\mathit{buildCache} (c : (\tau_c, \theta)) \Gamma &\triangleq \{(c, \emptyset, (\tau_c, \theta))\} \\
\mathit{buildCache} (x : (\tau_x, \theta)) \Gamma &\triangleq \{(x, [x \mapsto \tau_x], (\tau_x, \theta))\} \\
\mathit{buildCache} (\lambda_f x.e : (\tau_f, \theta_f)) \Gamma &\triangleq \{(\lambda_f x.e, \Gamma_{FV(\lambda_f x.e)}, (\tau_f, \theta_f))\} \cup (\mathit{buildCache} (f : (\tau_f, \theta_f)) \boxed{\Gamma}) \\
&\cup (\mathit{buildCache} (x : (\tau_x, \theta_x)) \boxed{\Gamma}) \cup (\mathit{buildCache} (e : (\tau_e, \theta_e)) \boxed{\Gamma[x \mapsto \tau_x, f \mapsto \tau_f]}) \\
\mathit{buildCache} (\mathbf{let} x = e_2 \mathbf{in} e_3 : (\tau_{let}, \theta_{let})) \Gamma &\triangleq \{(\mathbf{let} x = e_2 \mathbf{in} e_3, \Gamma_{FV(\mathbf{let} x = e_2 \mathbf{in} e_3)}, (\tau_{let}, \theta_{let}))\} \\
&\cup (\mathit{buildCache} (x : (\tau_x, \theta_x)) \boxed{\Gamma}) \cup (\mathit{buildCache} (e_2 : (\tau_2, \theta_2)) \boxed{\Gamma}) \\
&\cup (\mathit{buildCache} (e_3 : (\tau_3, \theta_3)) \boxed{\Gamma[x \mapsto \tau_x]}) \\
\mathit{buildCache} (e_1 \mathbf{op} e_2 : (\tau_{op}, \theta_{op})) \Gamma &\triangleq \{(e_1 \mathbf{op} e_2, \Gamma_{FV(e_1 \mathbf{op} e_2)}, (\tau_{op}, \theta_{op}))\} \\
&\cup (\mathit{buildCache} (e_1 : (\tau_1, \theta_1)) \boxed{\Gamma}) \cup (\mathit{buildCache} (e_2 : (\tau_2, \theta_2)) \boxed{\Gamma}) \\
\mathit{buildCache} (e_1 e_2 : (\tau_{app}, \theta_{app})) \Gamma &\triangleq \{(e_1 e_2, \Gamma_{FV(e_1 e_2)}, (\tau_{app}, \theta_{app}))\} \\
&\cup (\mathit{buildCache} (e_1 : (\tau_1, \theta_1)) \boxed{\Gamma}) \cup (\mathit{buildCache} (e_2 : (\tau_2, \theta_2)) \boxed{\Gamma})
\end{aligned}$$

Fig. 3: Definition of $\mathit{buildCache}$ for the incremental type inference of FUN.

Defining the shape of caches. Entries in the cache are $(e, \Gamma, (\tau, \theta))$ and a cache is

$$C \in \mathit{Cache} = \wp(\mathit{Expr} \times \mathit{Env} \times (\mathit{AType} \times \mathit{Subst}))$$

Building caches. The function $\mathit{buildCache}$ is easily defined in Fig. 3.

Incremental typing. In Fig. 4 we display the rules defining the algorithm \mathcal{IW} with judgements of the following form

$$\Gamma, C \vdash_{\mathcal{IW}} e : (\tau, \theta) \triangleright C'$$

Most of the rules mimic the behaviour of algorithm \mathcal{W} , following the templates of Section 3. Consider for example the rule (\mathcal{IW} -LET-MISS): first, the types of e_1 and e_2 are incrementally inferred in the environments prescribed by the relevant calls to the function tr . The result associated with the whole expression $\mathbf{let-in}$ is then the pair $(\tau_2, \theta_2 \circ \theta_1)$, where θ_1 and θ_2 are the substitutions obtained recursively from e_1 and e_2 , respectively.

Typing coherence. To prove the incremental algorithm \mathcal{IW} coherent with \mathcal{W} , we first show that compat_{env} satisfies Definition 1.

Lemma 2. *The predicate compat_{env} of Eq. (6) in Fig. 4 expresses compatibility.*

Again, the following theorem is an instance of Theorem 2, and follows from the above lemma.

Theorem 4. $\forall \Gamma, C, e. \Gamma \vdash_{\mathcal{W}} e : (\tau, \theta) \iff \Gamma, C \vdash_{\mathcal{IW}} e : (\tau, \theta) \triangleright C'$

6 Implementation and some experiments

We have implemented in OCaml our proposal making incremental the usage the type-checker of MinCaml [20].⁵ A formalization of MinCaml in our framework

⁵ Available at <https://github.com/mcaos/incremental-mincaml>

$$\begin{array}{c}
\text{(IW-HIT)} \quad \frac{\Gamma(e) = \langle \Gamma', (\tau, \theta) \rangle \quad \text{compat}_{env}(\Gamma, \Gamma', e)}{\Gamma, C \vdash_{\mathcal{IW}} e : (\tau, \theta) \triangleright C} \quad \text{(IW-CONST-MISS)} \quad \frac{\text{miss}(C, c, \Gamma) \quad \Gamma \vdash_{\mathcal{W}} c : (\tau, \theta) \quad C' = C \cup \{(c, \emptyset, (\tau, \theta))\}}{\Gamma, C \vdash_{\mathcal{IW}} c : (\tau, \theta) \triangleright C'} \\
\\
\text{(IW-VAR-MISS)} \quad \frac{\Gamma \vdash_{\mathcal{W}} x : (\tau, \theta) \quad C' = C \cup \{(x, \Gamma|_x, (\tau, \theta))\}}{\Gamma, C \vdash_{\mathcal{IW}} x : (\tau, \theta) \triangleright C'} \quad \text{miss}(C, x, \Gamma) \\
\\
\text{(IW-ABS-MISS)} \quad \frac{\boxed{\Gamma[x \mapsto \alpha_x, f \mapsto \alpha_x \rightarrow \alpha_e]}, C \vdash_{\mathcal{IW}} e : (\tau_e, \theta_e) \triangleright C'' \quad \boxed{\theta_1 = \mathcal{U}(\tau_e, \theta_e, \alpha_e) \wedge (\tau, \theta) = ((\theta_1(\theta_e \alpha_x)) \rightarrow (\theta_1 \tau_e), \theta_1 \circ \theta_e)} \quad C' = C'' \cup \{(\lambda_f x : \tau_x.e, \Gamma|_{FV(\lambda_f x : \tau_x.e)}, (\tau, \theta))\}}{\Gamma, C \vdash_{\mathcal{IW}} \lambda_f x.e : (\tau, \theta) \triangleright C'} \quad \text{miss}(C, \lambda_f x.e, \Gamma) \wedge \alpha_x, \alpha_e \text{ fresh} \\
\\
\text{(IW-OP-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\mathcal{IW}} e_1 : (\tau_1, \theta_1) \triangleright C'' \quad \boxed{\theta_1 \Gamma}, C \vdash_{\mathcal{IW}} e_2 : (\tau_2, \theta_2) \triangleright C''' \quad \boxed{\theta_3 = \mathcal{U}(\theta_2 \tau_1, \tau_{op}) \wedge \theta_4 = \mathcal{U}(\theta_3 \tau_2, \tau_{op}) \wedge (\tau, \theta) = (\tau_{res}, \theta_4 \circ \theta_3 \circ \theta_2 \circ \theta_1)} \quad C' = C'' \cup C''' \cup \{(e_1 \text{ op } e_2, \Gamma|_{FV(e_1 \text{ op } e_2)}, (\tau, \theta))\}}{\Gamma, C \vdash_{\mathcal{IW}} e_1 \text{ op } e_2 : (\tau, \theta) \triangleright C'} \quad \text{miss}(C, e_1 \text{ op } e_2, \Gamma) \wedge \tau_{op}, \tau_{res} = \{\text{int}, \text{bool}\} \\
\\
\text{(IW-APP-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\mathcal{IW}} e_1 : (\tau_1, \theta_1) \rightarrow \tau_e \triangleright C'' \quad \boxed{\theta_1 \Gamma}, C \vdash_{\mathcal{IW}} e_2 : (\tau_2, \theta_2) \triangleright C''' \quad \boxed{\theta_3 = \mathcal{U}(\theta_2 \tau_1, \tau_2 \rightarrow \alpha) \wedge (\tau, \theta) = (\theta_3 \alpha, \theta_3 \circ \theta_2 \circ \theta_1)} \quad C' = C'' \cup C''' \cup \{(e_1 e_2, \Gamma|_{FV(e_1 e_2)}, (\tau, \theta))\}}{\Gamma, C \vdash_{\mathcal{IW}} e_1 e_2 : (\tau, \theta) \triangleright C'} \quad \text{miss}(C, e_1 e_2, \Gamma) \wedge \alpha \text{ fresh} \\
\\
\text{(IW-IF-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\mathcal{IW}} e_1 : (\tau_1, \theta_1) \triangleright C'' \quad \boxed{\theta_1 \Gamma}, C \vdash_{\mathcal{IW}} e_2 : (\tau_2, \theta_2) \triangleright C''' \quad \boxed{\theta_2(\theta_1 \Gamma)}, C \vdash_{\mathcal{IW}} e_3 : (\tau_3, \theta_3) \triangleright C^{iv} \quad \boxed{\theta_4 = \mathcal{U}(\theta_3(\theta_2 \tau_1), \text{bool}) \wedge \theta_5 = \mathcal{U}(\theta_4 \tau_3, \theta_4(\theta_3 \tau_1)) \wedge (\tau, \theta) = (\theta_5(\theta_4 \tau_3), \theta_5 \circ \theta_4 \circ \theta_3 \circ \theta_2)} \quad C' = C'' \cup C''' \cup C^{iv} \cup \{(\text{if } e_1 \text{ then } e_2 \text{ else } e_3, \Gamma|_{FV(\text{if } e_1 \text{ then } e_2 \text{ else } e_3)}, (\tau, \theta))\}}{\Gamma, C \vdash_{\mathcal{IW}} \text{if } e_1 \text{ then } e_2 \text{ else } e_3 : (\tau, \theta) \triangleright C'} \quad \text{miss}(C, \text{if } e_1 \text{ then } e_2 \text{ else } e_3, \Gamma) \\
\\
\text{(IW-LET-MISS)} \quad \frac{\boxed{\Gamma}, C \vdash_{\mathcal{IW}} e_2 : (\tau_2, \theta_2) \triangleright C'' \quad \boxed{\theta_1 \Gamma}[x \mapsto \tau_2], C \vdash_{\mathcal{IW}} e_3 : (\tau_3, \theta_3) \triangleright C''' \quad \boxed{(\tau, \theta) = (\tau_3, \theta_3 \circ \theta_1)} \quad C' = C'' \cup C''' \cup \{(\text{let } x = e_2 \text{ in } e_3, \Gamma|_{FV(\text{let } x = e_2 \text{ in } e_3)}, (\tau, \theta))\}}{\Gamma, C \vdash_{\mathcal{IW}} \text{let } x = e_2 \text{ in } e_3 : (\tau, \theta) \triangleright C'} \quad \text{miss}(C, \text{let } x = e_1 \text{ in } e_3, \Gamma)
\end{array}$$

with $\text{compat}_{env}(\Gamma, \Gamma', e) \triangleq \text{dom}(\Gamma) \supseteq FV(e) \wedge \text{dom}(\Gamma') \supseteq FV(e) \wedge \forall y \in FV(e). \mathcal{U}(\Gamma(y), \Gamma'(y))$ (6)

Fig. 4: Rules defining incremental algorithm \mathcal{IW} to infer FUN types.

and all tables and results of our experiments are available online in the extended version [3]. In detail, caches and type environments are implemented as hash-tables, so their handling is done almost in constant time. The memory overhead due to the cache is $\mathcal{O}(n \times m)$, where n is the size of the program under analysis and m is the number of variables therein.

The other possible time consuming part concerns checking environment compatibility. The key idea to make compat_{env} efficient is to compute the sets of the free variables beforehand, and to store them as additional annotations on

the aAST. Summing up, implementing our schema is not too demanding, since it can be done with standard data structures.

Next, we show that (i) the cost of using the type checker incrementally depends on the size of *diffs*; (ii) its performance increases as these become smaller; and (iii) the incremental usage is almost always faster than re-using the standard one. The comparison is done by type checking synthetic programs with (binary and complete) aAST of increasing depth from 8 to 16, and with a number of variables ranging from 1 to 2^{15} . All the internal nodes are binary operators and the leaves are free variables. This test suites are intended to stress our incremental algorithm in the worst, yet artificial case. The measures are obtained using the library *Benchmark* that takes into account the overhead of OCaml runtime.⁶

To test the efficiency of caching we first re-typed twice the program with no changes, starting with an empty cache. We considered (binary and complete) aAST of depth 16, and we collected the number of re-typings per second in function of the number of variables in the program, ranging from 1 to 2^{15} .

The experiments show not only that the overhead for caching is negligible but also that caching is beneficial when the number of free variables is not too large w.r.t. the aAST depth because the results of common subtrees are re-used.

Then, we have simulated program changes by invalidating parts of caches that correspond to the rightmost subexpression at different depths. Note that invalidating cache entries for the *diff* subexpression e' of e requires to invalidate (i) all the entries for the nodes in the path from the root of the aAST of e to e' and (ii) all the entries for e' and its subexpressions, recursively. We collected the number of re-typings per second vs. the size of the *diff* for a few choices of aAST depth (from 12 to 16) and number of variables (from 2^7 to 2^{16}). The experimental results show that our caching and memoization is faster than re-typing twice. An exception is when aAST have the maximum number of variables and the considered changes exceed 25% of the nodes. All in all, the advantage of using incrementally a type checker decreases, as expected, when there is a significant growth of the number of variables or in the size of the program. However, these cases only show up with very big numbers, which are not likely to occur often.

We also measured the number of MBs allocated by our implementation for the standard type checking and by its incremental usage with respect to the size of the synthetic aAST and the number of free variables.⁷ We considered aAST of depth ranging from 10 to 16 and the number of variables ranging from 2^9 and 2^{15} , and the results show that the ratio standard/incremental is .99, almost constant.

7 Conclusions

We have presented an algorithmic schema for incrementally using existing type checking and type inference algorithms. Since only the shape of the input, the output, and some domain-specific knowledge of the original algorithms are relevant, our schema considers them as grey-boxes. Remarkably, the only real

⁶ Available at <https://github.com/Chris00/ocaml-benchmark>

⁷ As measured by the Landmarks library <https://github.com/LexiFi/landmarks>

effort for defining the incremental algorithm is required for establishing the notion of compatibility between parts of the environments relevant for re-typing. We have introduced the basic bricks of our approach and proved a theorem guaranteeing the coherence of *any* original algorithm with its incremental version, and *vice versa*. As a matter of fact, coherence follows from easily checking a mild condition on the environment compatibility. To illustrate the approach we have then instantiated our proposal for checking non-interference within an imperative language and for type inference within a functional language.

We have implemented the incremental version of the type checker of MinCaml, and we have assessed it on synthetic programs with varying size and number of variables. The experiments have shown our proposal worth using within a continuous software development model where fast responsiveness is needed, because only *diffs* are typed, possibly with those parts of the code affected by them. Additionally, the cost of using the type checker incrementally depends on the size of *diffs*, and its performance increases as these become smaller, a typical situation when applying local transformations, e.g. code motion, dead code elimination, and code wrapping.

Future work. We are confident that little extensions to our proposal are needed to cover also type and effect systems. Also, other programming paradigms should be easily accommodated in our incremental schema, as preliminary results on process calculi suggest. More work is instead required to apply our ideas to other syntax-directed static analyses, e.g. control flow analysis because of fixed-point computations. We also plan to carry our proposal on Abstract Interpretation, where the rich structure of the abstract domains poses some serious challenges. Presently, we are extending our prototype with an incremental type inference for MinCaml. Moreover, we plan to further automatize our proposal by mechanically deducing the relation $compat_{env}$, based on the syntax and on some relevant aspects of types, e.g. sub-typing. Since tr and $checkJoin$ are directly inherited from the given type checking or inference algorithm, one can implement a generator that automatically produces its corresponding incremental version.

More experiments on real programs are also in order to better assess the performance of our proposal, as well as its scalability.

Finally, we would also like to apply the incremental schema to real-world languages, e.g. OCaml.

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